



CYPR-CD01209M

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	
Nemecek,)	Examiner: Thompson, A. M.
)	
Serial No.: 09/998,859)	
)	Art Unit: 2825
)	
Filing Date: November 15, 2001)	
)	
FOR: A SYSTEM AND A METHOD FOR CHECKING)	
LOCK STEP CONSISTENCY BETWEEN AN)	
IN CIRCUIT EMULATION AND A)	
MICROCONTROLLER WHILE DEBUGGING)	
PROCESS IS IN PROGRESS)	
)	

**DECLARATION UNDER 37 C.F.R. §1.132 TRAVERSING GROUNDS OF REJECTION
UNDER 35 U.S.C. § 102(a)**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

I, Craig Nemecek, do hereby declare and say:

My home address is 2131 52nd Ave. S.W. Seattle, WA 98116.

I have been employed by Cypress Microsystems since April 10, 2000. I am currently a Staff Design Engineer, a position I have held since April 10, 2000.

On November 14, 2001 I signed a declaration stating that I believe that I am the original and sole inventor of the subject matter that is claimed in U.S. Patent Application No.

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09/998,859, entitled, "A System and a Method for Checking Lock Step Consistency Between an In Circuit Emulation and a Microcontroller while Debugging Process is in Progress (hereinafter, the present Patent Application). The present Patent Application was filed in the United States Patent and Trademark Office on November 15, 2001. The declaration stating that I believe that I am the original and sole inventor of the subject matter claimed in the present Patent Application was filed in the United States Patent and Trademark Office, along with the present Patent Application.

I have studied the entire specification, drawings, and claims of the present Patent Application. I understand the language of the claims and the scope thereof. I understand the invention as claimed.

As an employee of Cypress Microsystems, a subsidiary of Cypress Semiconductor, subject matter that I originate may be incorporated into Cypress documents, some of which may become public.

I have reviewed version 1.11 of the document "PSoC Designer: Integrated Development Environment User Guide" (hereinafter, IDE User Guide). It is my belief that, to the extent that subject matter exists in the document "IDE User Guide" that may anticipate claims of the present Patent Application, such information was derived from my own work.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of

Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Respectfully submitted,

Date: 1/22, 2004

Signature

Full Name: Craig Nemecek